Oct. - Nov. - 2015

204-Digital Computer Organization BCA Sem-2

Total Marks: 70		code:2917	Time: 2.5 Hou	2.5 Hours	
Q-1 (A)	Answer the following What is Gate? Explain Boolean function	any three gates with truth table, G	braphical symbol and	07	
(B)	Which gate is known as Universal Gate? Explain Universal Gates in detail OR			07	
Q-1	Answer the following				
(A)	Explain De Morgan's T		T (0 4 5 5 T)	07	
(B)	Obtain Simplify Expres	sion and Draw Circuit: F(A,B.C)	$= \sum (0,1,2.3,7)$	07	
Q – 2	Answer the following				
(A)		Circuit? Explain 8 * 3 Encoder.		07	
(B)	Explain 6 bit Comparate			07	
Q – 2	Answer the following	OR			
(A)	_	in types of IC chip in detail.		07	
(B)	4 4	iplexer? Explain 1 bit – 4 line mul	tiplexer.	07	
Q-3	Answer the following		÷		
(A)		cuit? Explain Half Adder in detail		07	
(B)	Explain Shift Left and S	Shift Right operation using 1 bit sh OR	iifter	07	
Q-3	Answer the following			^-	
(A)				07	
(B)	Explain 4 bit Binary Ad	ider in detail		07	
Q – 4	Answer the following	•	•		
(A)		nd ROM. Differentiate Volatile an	d Non Volatile	07	
(B)	Memory Define Sequential Circu	uit. Explain Clocked RS Flip Flop		07	
(2)		OR			
Q – 4	Answer the following		•		
(A)	Explain D and JK Flip I	_		07	
(B)	Explain Shift Register	in detail.		. 07	
Q-5	Answer the following	·			
(A)	Explain General Microp			07	
(B)	Explain Bus Clicking in			07	
0 5	Answer the following	OR			
Q-5 (A)	Explain Bus Arbitration	in detail		07	
(B)	Explain ISA and USB I			07	
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