B.Sc	. Sen	n. V (PHYSICS) Paper :504	 1		
Title	<u> </u>	: Digital Electronics and Solid state devices			
Code : 4297 NOV 2014					
Que.1	[a]	What is universal gate? Explain NAND gate as an universal gate with necessary block diagram.	[08]		
	[b]	Explain AND gate with transistor circuit. OR	[06]		
Que.1	[a] [b]	What is called logic circuit? Explain diode based OR gate. What is NOT gate? Explain it as transistor circuit.	[07] [07]		
Que.2		Write Boolean expression, reduce it using mapping and implement in NOR logic: [14] F= m (2,4,6,8,10,13,14,15) using SOP method			
Que.2		OR Write Boolean expression, reduce it using mapping and implement in NOR logic:	[14]		
	4	F= m (2,4,6,7,12,13,14,15) using POS method			
Que.3	[a]	Find the minterms for the following and also state their designation [1]A + BC	[12]		
		[2] AB + ACD			
		[3] AB+C			
	[b]	[4] ABC + CD Find designated value of \overline{WX} Y \overline{Z} OR	[02]		
Que.3	[a]	Reduce the following expressions to the simplest form and implement in AND/OR/invert logic.	[12]		
		[1] $A + \overline{A}B + AB$			
		$[2]A\overline{B} + \overline{A}B + AB + \overline{A}\overline{B}$			
i		$[3]A\overline{B}D + A\overline{B}D + \overline{B}\overline{D}$			
٠		[4] (AB + C)(AB + D)			
	[b]	Write De Morgan's theorem and verify it with truth table.	[02]		
Que.4	[a]	Without reducing convert the following expression to AND,OR and NOT logic. Then convert it in NAND logic circuit (two INPUT) Mention number of gates used. [1] A + B + AB	[10]		
		[2] $(A + (A + \overline{B} + C)(A\overline{BC})$			
•		[b] Write application of LED.	[04]		

Que.		t is DIAC? Describe its construction, characteristic and ope essary circuit diagram.	ration with	[80]
[b]	How a Bipola	r transistor can be used as ac and dc switch?	[06]	
Que.5[a]Explain full a	dder with block diagram.	[06]	
[b]	What is IC? Explain classification of IC's by function briefly.			
		OR		
Que.5[[b]	a] Writ Explain photo	e short note on diode as an ac switch. diode.	[06]	[80]